A Selected List of Resources
On
Very Large Scale Integration (VLSI)

A Bibliography
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Resource Centre

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1.	**Algorithms For VLSI Desgin Automation** Gerez, Sabith H. Acc. No. 004841 621.395 GER
2.	**Algorithms for VLSI physical design automation, 3rd ed.** Sherwani, Naveed . Acc. No. 013449 621.395 SHE
3.	**Analog Design for CMOS VLSI Systems** Maloberti, Franco Acc. No. 002751 621.3815 MAL
4.	**Analog VLSI signal and information processing** Ismail, Mohammed (under process)
5.	**ApplicationSpecific Integrated Circuits** Smith, Michael John Sebastian Acc. No. 000779 621.382 SMI
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12.	**CMOS Current Amplifiers** Palmisano, Giuseppe, Palumbo, Gaetano and Pennisi, Salvatore/ Acc. No. 002761 621.381 PAL
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14.	**CMOS VLSI design : a circuits and systems perspective, 3rd. ed** Weste, Neil H. E., Harris, David & Banerjee, Ayan 621.395 WES 020089 - 020092
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16.	**Computer Aided Logical Design With Emphasis On VLSI** Hill, Frederick J.. Acc. No. 004451 621.395 HIL
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2.	Integration, the VLSI http://www.sciencedirect.com/science/journal/01679260
3.	Journal of VLSI Signal Processing http://www.springerlink.com/content/100304
4.	IEEE Circuits and Devices Magazine, http://ieeexplore.ieee.org/xpl/RecentIssue.jsp?punumber=101
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1.	**GLVLSI** Great Lakes Symposium on VLSI

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13.	VLSI Design, CSI/IEEE International Symposium
14.	VLSI Design, International Conference
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17.	VLSI Multilevel Interconnection, International IEEE Conference
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1.	**Fractal Based Approach for Face Recognition** Athale, Suprita (200211035) 006.4 ATH C01365
2.	**Design of frequency synthesizable delay locked loop** Shah, Hardik K (200211030) 621.3815364 SHA C01503
3.	**Design and Synthesis of Asynchronous Circuits** Asthana, Abhinav (200411033) 621.3815 AST C01171
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